

Electronic Acknowledgement Receipt

EFS ID:	1025110
Application Number:	10595424
Confirmation Number:	9229
Title of Invention:	SEMICONDUCTOR CHIP MOUNTED INTERPOSER, SEMICONDUCTOR DEVICE, SEMICONDUCTOR CHIP INTERPOSER FABRICATION METHOD, BARE CHIP MOUNTED INTERPOSER, AND INTERPOSER SHEET
First Named Inventor:	Moriyoshi NAKASHIMA
Customer Number:	43076
Filer:	Mark D. Saralino
Filer Authorized By:	
Attorney Docket Number:	KOMOP0113US
Receipt Date:	18-APR-2006
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Application Type:	U.S. National Stage under 35 USC 371
International Application Number:	PCT/JP03/16012

Payment information:

Submitted with Payment	yes
Payment was successfully received in RAM	\$1100.0
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File Listing:

Document Number	Document Description	File Name	File Size(Bytes)	Multi Part	Pages

1		KOMOP0113USapplication.pdf	9930388	yes	155
Multipart Description					
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Warnings:

Information:

2	Fee Worksheet (PTO-875)	fee-info.pdf	8583	no	2
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Warnings:

Information:

Total Files Size (in bytes):	9938971
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New Applications Under 35 U.S.C. 111

If a new application is being filed and the application includes the necessary components for a filing date (see 37 CFR 1.53(b)-(d) and MPEP 506), a Filing Receipt (37 CFR 1.54) will be issued in due course and the date shown on this Acknowledgement Receipt will establish the filing date of the application.

National Stage of an International Application under 35 U.S.C. 371

If a timely submission to enter the national stage of an international application is compliant with the conditions of 35 U.S.C. 371 and other applicable requirements a Form PCT/DO/EO/903 indicating acceptance of the application as a national stage submission under 35 U.S.C. 371 will be issued in addition to the Filing Receipt, in due course.